

# VLSI Design: Theory And Practice By Nidhi Syal;Vikrant Viz

**By Nidhi Syal;Vikrant Viz**

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Real World FPGA Design with Verilog. Ken Coffman. Verlag: Prentice Hall. ISBN 10: 0130998516 ISBN 13: 9780130998514. Gebraucht Paperback  
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<http://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.124.3689>

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and there exists one or more paths corresponding to worst-case delay. Analog Design Environment Theory and Practice By Vikrant Viz, Nidhi Syal  
<http://electronics.stackexchange.com/questions/131100/simulation-to-find-worst-case-delay-path>

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